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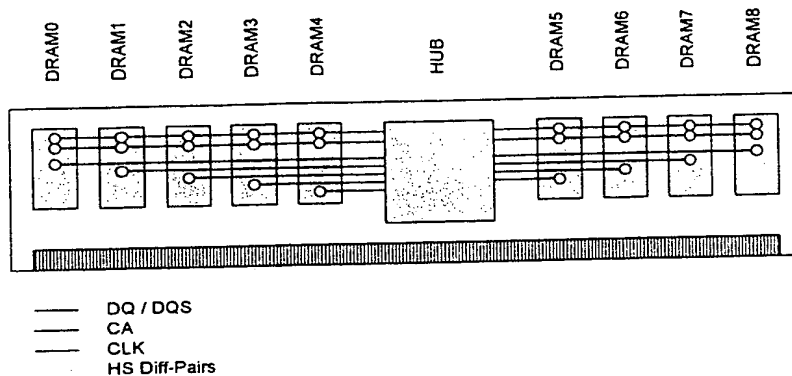


Figure 1: DIMM reference model

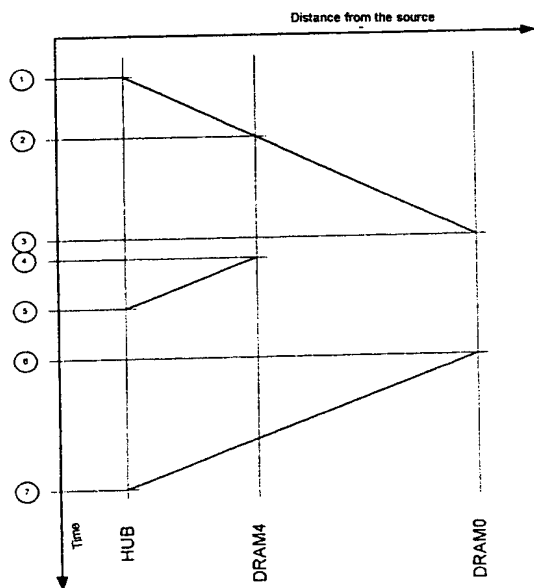


Figure 2: Signal propagation on a DIMM

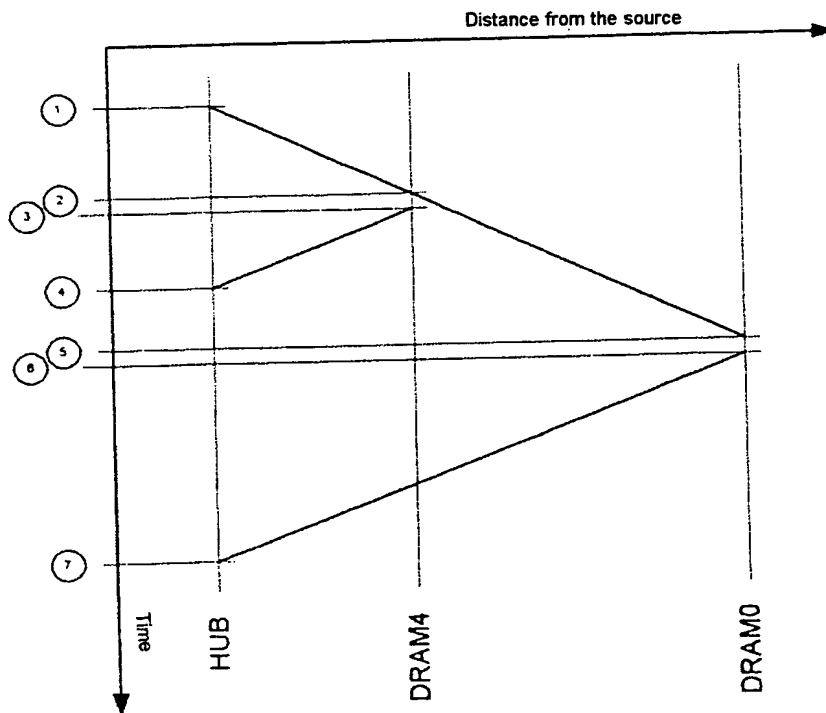


Figure 3: Timing diagram for delay time compensation

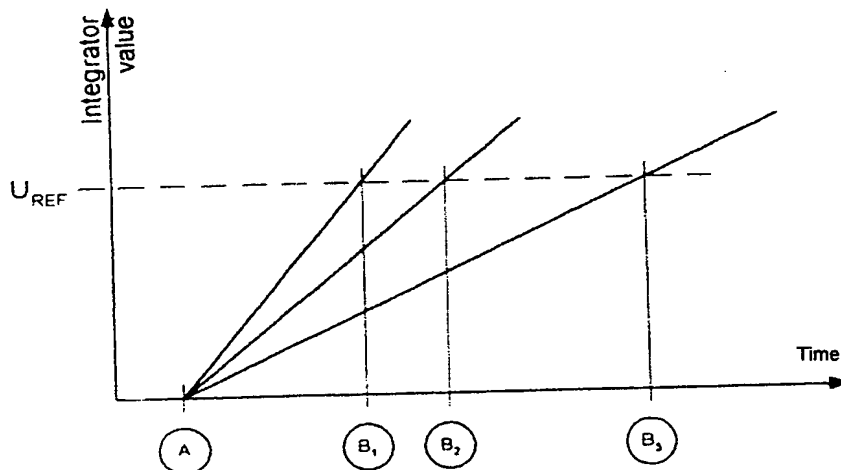


Figure 4: Characteristic diagram of controllable integrators

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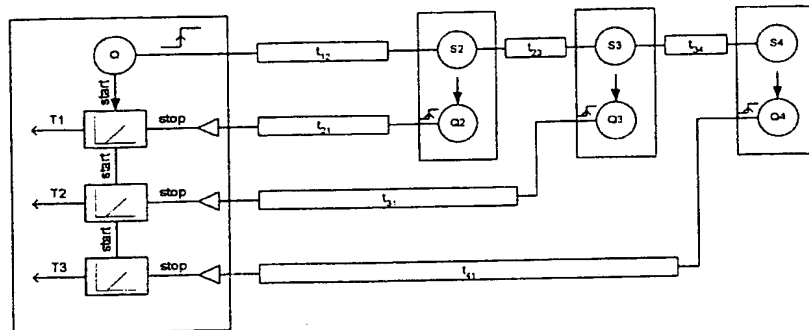


Figure 5: Illustration of the source and sinks involved

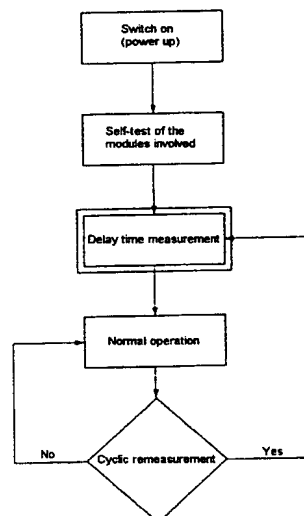


Figure 6: Flowchart for delay time compensation